

## EAST Search History

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| L1    | 8    | redundancy and fuse and address and data adj latch and compar\$3 with address and tester and defective adj cell | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/24 09:39 |
| L2    | 7    | 1 and first and second and third  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/24 09:37 |
| L3    | 0    | 1 and first adj address and second adj address and third adj address  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/24 09:35 |
| L4    | 0    | 1 and first adj address\$2 and second adj address\$2 and third adj address\$2                                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/24 09:35 |
| L5    | 0    | 1 and second adj address\$2 and third adj address\$2  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/24 09:35 |
| L6    | 0    | 1 and second adj address\$2   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/24 09:36 |
| L7    | 0    | 1 and second adj addresses  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/24 09:36 |
| L8    | 0    | 1 and second adj addresses  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/24 09:36 |

## EAST Search History

|     |    |   |   |    |    |                  |
|-----|----|---|---|----|----|------------------|
| L9  | 8  | 1 and logic   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/24 09:38 |
| L10 | 1  | 1 and laser   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/24 10:09 |
| L11 | 0  | program\$4 with defective adj<br>address with fuse with after with<br>test with cut with laser.clm. | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/24 10:11 |
| L12 | 54 | latch\$3 with defective adj address<br>with data adj latch  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/24 10:12 |
| L13 | 12 | 12 and compar\$3 with input adj<br>address with defective adj address                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/24 10:16 |
| L14 | 0  | 13 and test with redundancy adj<br>cell   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/24 10:13 |
| L15 | 12 | 13 and fuse   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/24 10:16 |
| L16 | 5  | 15 and laser  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/24 10:16 |